

Advanced Analog CMOS IC Design

EPFL, LAUSANNE, SWITZERLAND AUGUST 27-31, 2018

MONDAY, August 27		
8:30-12:00 am	MOS and Bipolar: Modes of Operation and Models	Eric Vittoz
1:30-3:00 pm	Elementary Building Blocks	Eric Vittoz
3:30-5:00 pm	Voltage References	Eric Vittoz
TUESDAY, Au	igust 28	
8:30-12:00 am & 1:30-5:00 pm	Analog Functional Blocks	Willy Sansen
WEDNESDAY	, August 29	
8:30-12:00 am	Continuous Time Filters	Boris Murmann
1:30-5:00 pm	Switched-Capacitor Interface Circuits	Boris Murmann
THURSDAY, A	August 30	
8:30-10:00 am	Switched-Capacitor Interface Circuits	Boris Murmann
10:30-12:00 am & 1:30-5:00 pm	CMOS Off-Chip Drivers	Rinaldo Castello
FRIDAY, Augu	ust 31	
8:30-12:00 am & 1:30-3:00 pm	Technology and other Limitations for Analog Design	Herman Casier