



## Low-Power Analog IC Design

EPFL, LAUSANNE, SWITZERLAND  
AUGUST 29 – SEPTEMBER 2, 2022

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### MONDAY, August 29

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8:30-12:00 am	MOS Transistor Modeling for Low-Voltage and Low-Power Circuit Design	Christian Enz
1:30-3:00 pm	Basic Low-Power Low-Voltage Circuit Techniques	Willy Sansen
3:30-5:00 pm	Differential Amplifying Blocks with Positive Feedback	Willy Sansen

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### TUESDAY, August 30

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8:30-10:00 am	Noise Performance of Elementary Transistor Stages	Willy Sansen
10:30-12:00 am	Stability of Operational Amplifiers	Willy Sansen
1:30-3:00 pm	Systematic Design of Low-Power Operational Amplifiers	Willy Sansen
3:30-5:00 pm	Important Opamp Configurations	Willy Sansen

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### WEDNESDAY, August 31

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8:30-10:00 am	Fully-Differential Operational Amplifiers	Willy Sansen
10:30-12:00 am	Bandgap and Current Reference Circuits	Willy Sansen
1:30-5:00 pm	Design of Low-Power Analog Circuits using the Inversion Coefficient	Christian Enz

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### THURSDAY, September 1

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8:30-10:00 am	Distortion in Elementary Transistor Circuits	Willy Sansen
10:30-12:00 am	Low-Power Continuous-Time Filters	Willy Sansen
1:30-3:00 pm	Practical Techniques of Frequency Compensation	Vadim Ivanov
3:30-5:00 pm	Nanopower Design Techniques & Efficient Energy Harvesting	Vadim Ivanov

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### FRIDAY, September 2

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8:30-12:00 am	Micropower ADCs	Kofi Makinwa
1:30-5:00 pm	Matching of MOS Transistors in Deep-Submicron	Marcel Pelgrom

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